HECEIVED

JUL 2 6 2001



Technology Center 2100

Sheet 1 of 1



"Double Data Rate (DDR)

x4, x8, x16 DDR SDRAM,

C.c.

INFORMATION DISCLOSURE STATEMENT
BY APPLICANT

(Use several sheets if necessary)

Atty. Docket No.: 303.736US1

Serial No. 09/754,972

Applicant: Aaron M. Schoenfeld et al.

Filing Date: January 5, 2001

Group: Unknown

TT.S.	ΡΔΥΚΝΥ	DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate		
C.C.	_ 4,949,361	08/14/1990	Jackson, R.	375	118	06/26/89		
C.C.	_ 5,692,165	11/25/1997	Jeddeloh, J., et al.	395	551	09/12/95		
C.C.	_ 5,812,462	09/22/1998	Merritt, T.	365	189.05	04/03/97		
c.c.	_ 5,819,076	10/06/1998	Jeddeloh, J., et al.	395	552	06/25/97		
	_ 5,852,378	12/22/1998	Keeth, B.	327	171	02/11/97		
	_ 5,857,095	01/05/1999	Jeddeloh, J.M., et al.	395	552	06/25/97		
C.C.	_ 5,875,134	02/23/1999	Cloud, E.H.	365	193	09/08/97		
	_ 5,886,948	03/23/1999	Ryan, K.J.	365	233	10/29/97		
C.C.	_ 5,920,518	07/06/1999	Harrison, R.M., et al.	365	233	02/11/97		
c. <i>C</i> .	_ 5,923,611	07/13/1999	Ryan, K.J.	365	233	12/20/96		
	_ 5,930,198	07/29/1999	Ryan, K.	365	233	10/29/97		
c.c.	_ 5,936,893	08/10/1999	Merritt, T.	365	189.05	07/02/98		
C.C.	_ 5,986,948	11/16/1999	Cloud, E.H.	365	193	09/23/98		
c.C.	_ 6,026,051	02/15/2000	Keeth, B., et al.	365	233	03/24/99		
<u> </u>	_ 6,069,506	05/30/2000	Miller, Jr., J., et al.	327	156	05/20/98		
<u> </u>	_ 6,069,510	05/30/2000	Keeth, B.	327	170	11/25/98		
	_ 6,100,733	08/08/2000	Dortu, J., et al.	327	149	06/09/98		
<u>C · C.</u>	_ 6,104,209	08/15/2000	Keeth, B., et al.	326	83	08/27/98		
C.C.	_ 6,104,643	08/15/2000	Merritt, T.	365	189.05	08/10/99		
<u>C.C.</u>	_ 6,111,446	08/29/2000	Keeth, B.	327	258	03/20/98		
<u>C.c.</u>	_ 6,137,334	10/24/2000	Miller, Jr., J., et al.	327	271	07/06/98		
C.C.	_ 6,243,797	06/05/2001	Merritt, T.	711	167	02/18/97		
		FC	REIGN PATENT DOCUMENTS					
aminer itial	Document Number	Date	Country	Class	Subclass	Translation Yes No		
kaminer		(Including	OTHER DOCUMENTS Author, Title, Date, Pertinent Pages, Ed	cc.)				
		"DDR SDRAM Functionality and Controller Read Data Capture", DesignLine,						
C.C.	Volume 8,	Volume 8, Issue 3, 3Q99, Micron Technology, Inc., pp. 1-24, (1999)						

Examiner Oh. Mr. Chang	Date Considered 3/12/04
*Substitute Disclosure Statement Form (PTO-1449)	

(2000)

pp. 1-66,

Sdram", Micron Semiconductor Products, Inc., 64Mb:

^{**}EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.